

FIG.2

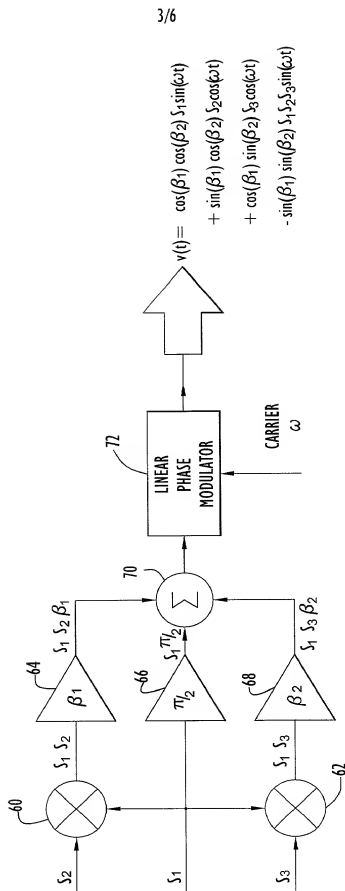


FIG.3

PRIOR ART

INTERPLEX MODULATOR

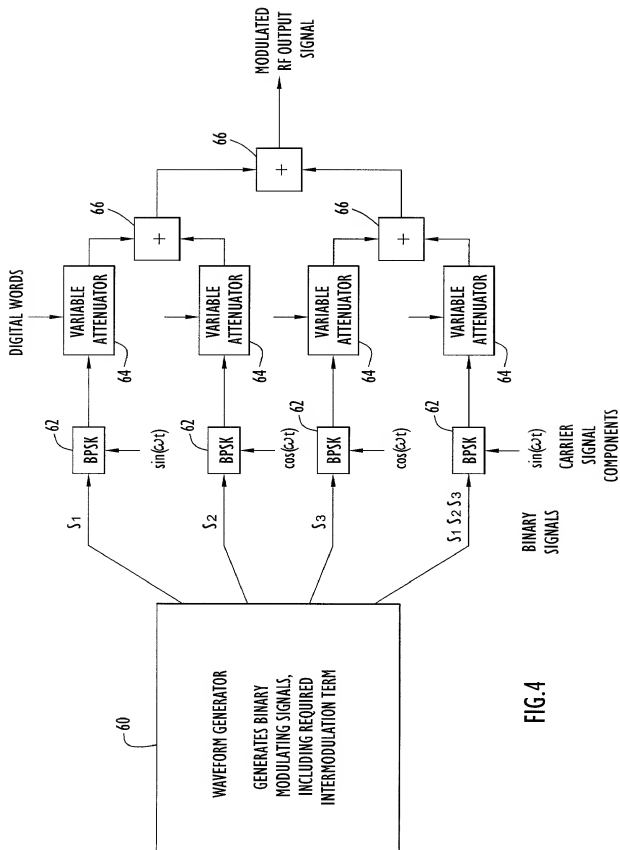


FIG. 4

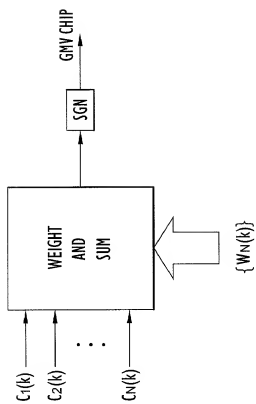


FIG. 5

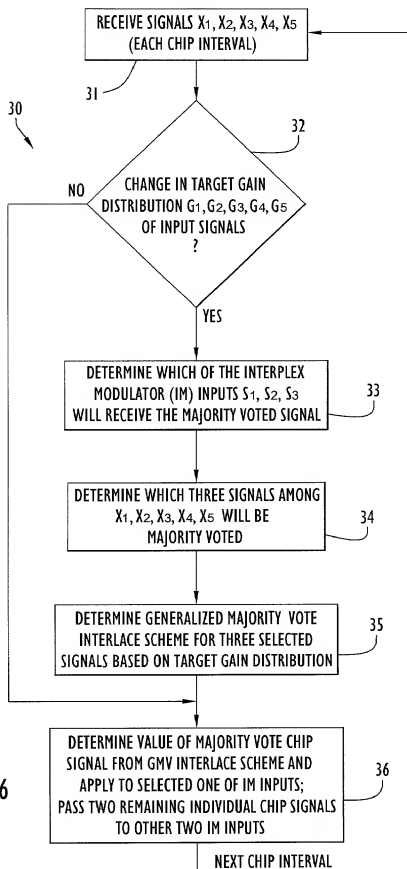


FIG. 6